



Transient Tests

Trise, Tfall, Trisdelay, Tfalldelay vs Vdd at 3 temp

Vdd sweep from 4.5V to 18 in 0.5V steps

Repeat at -40C 25C 125C. Can't do

this in one run due to Orcad limitation

Risetime_NoOvershoot(v(g))

Falltime_NoOvershoot(v(g))

XatNthY_PercentYRange(v(g),90,1)-XatNthY_PercentYRange(v(in),10,1)

XatNthY_PercentYRange(v(g),10,2)-XatNthY_PercentYRange(v(in),90,2)

When overshoot exists, parametric measurements not correct

Title		
11_Tr_vs_Vdd		
Size	Document Number	Rev
A	Driver Test Circuit	0
Date:	Sunday, October 25, 2009	Sheet 1 of 1